



JCT Rec'd PCT/PTO 30 APR 2002

ocket No. 43890-555

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Toyonori KANETAKA, et al.

Serial No.: 10/009,750

Group Art Unit:

Filed: December 12, 2001

Examiner:

For: METHOD OF MANUFACTURING CHIP INDUCTOR

**RESPONSE TO NOTICE
OF MISSING REQUIREMENTS**

Box Missing Parts
Commissioner for Patents
Washington, DC 20231

Sir:

In response to the Notice of Missing Requirements dated March 6, 2002, submitted
herewith are the following documents for filing in the above-referenced application:

1. Declaration and Power of Attorney (which is in compliance with 37 CFR 1.63)
2. Assignment
3. Preliminary Amendment
4. Substitute Specification and Marked-Up Version of the Substitute Specification
5. Request for Approval of Drawings Amendment and Clean Version of Drawing
Amendment

Please charge Deposit Account No. 500417 in the amount of . To the extent necessary,
Applicants petition for an extension of time under 37 C.F.R. 1.136. Please charge any shortage



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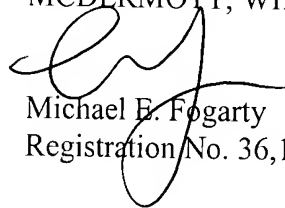
PATENT TRADEMARK OFFICE

in fees due in connection with the filing of this paper, including extension of time fees, to
Deposit Account 500417 and please credit any excess fees to such deposit account.

It is requested that the official filing receipt now be issued.

Respectfully submitted,

MCDERMOTT, WILL & EMERY



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Date: April 30, 2002